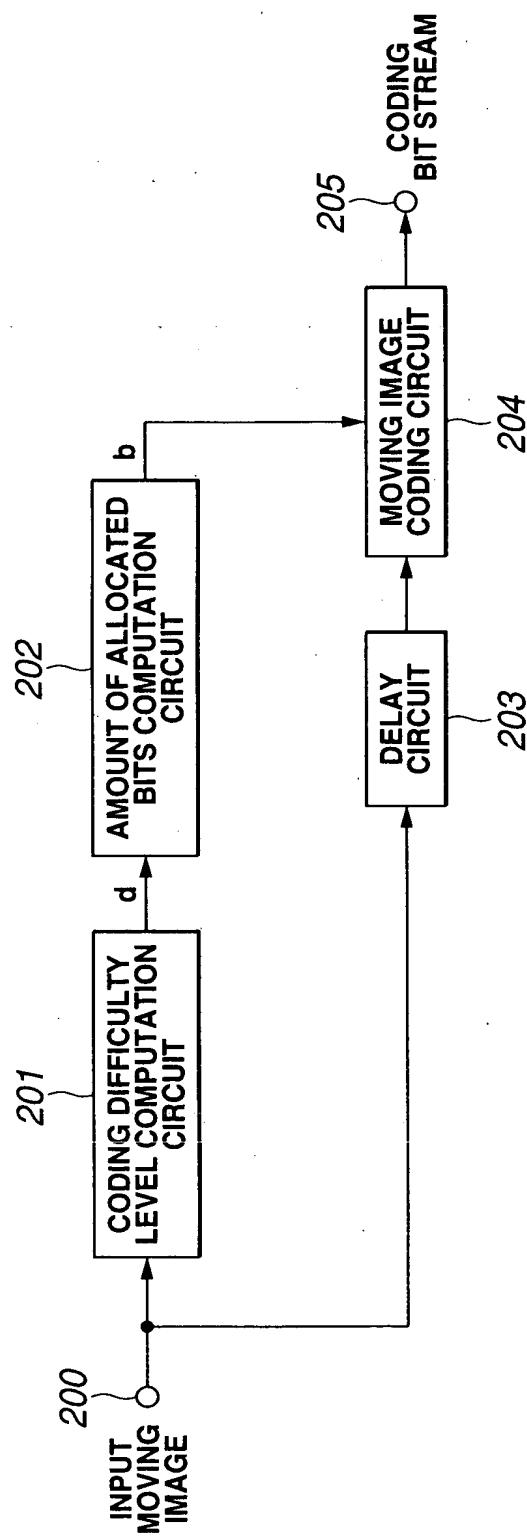


1000110110001100

**FIG.1**

09505610270

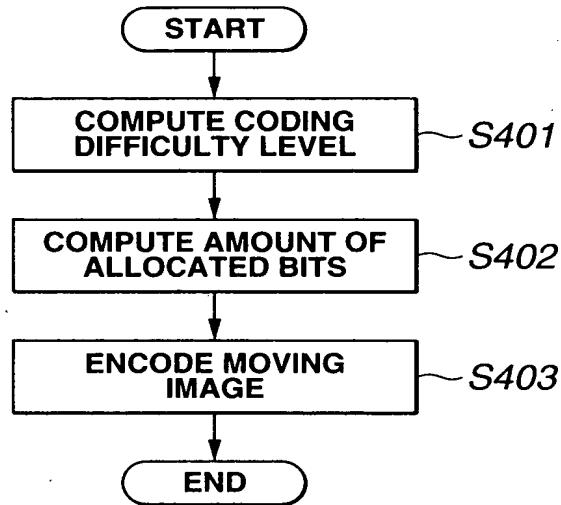


FIG.2

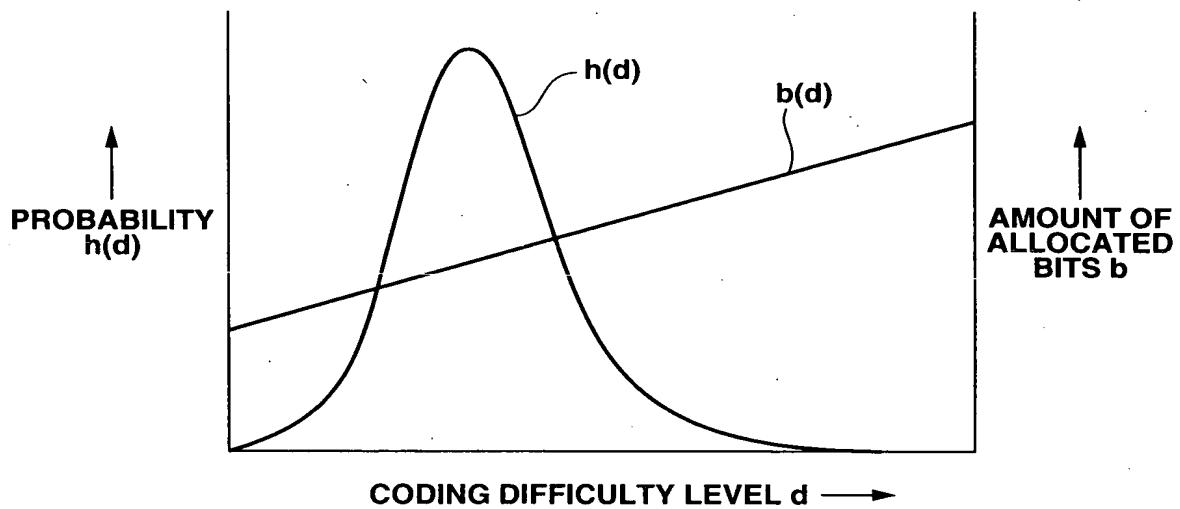
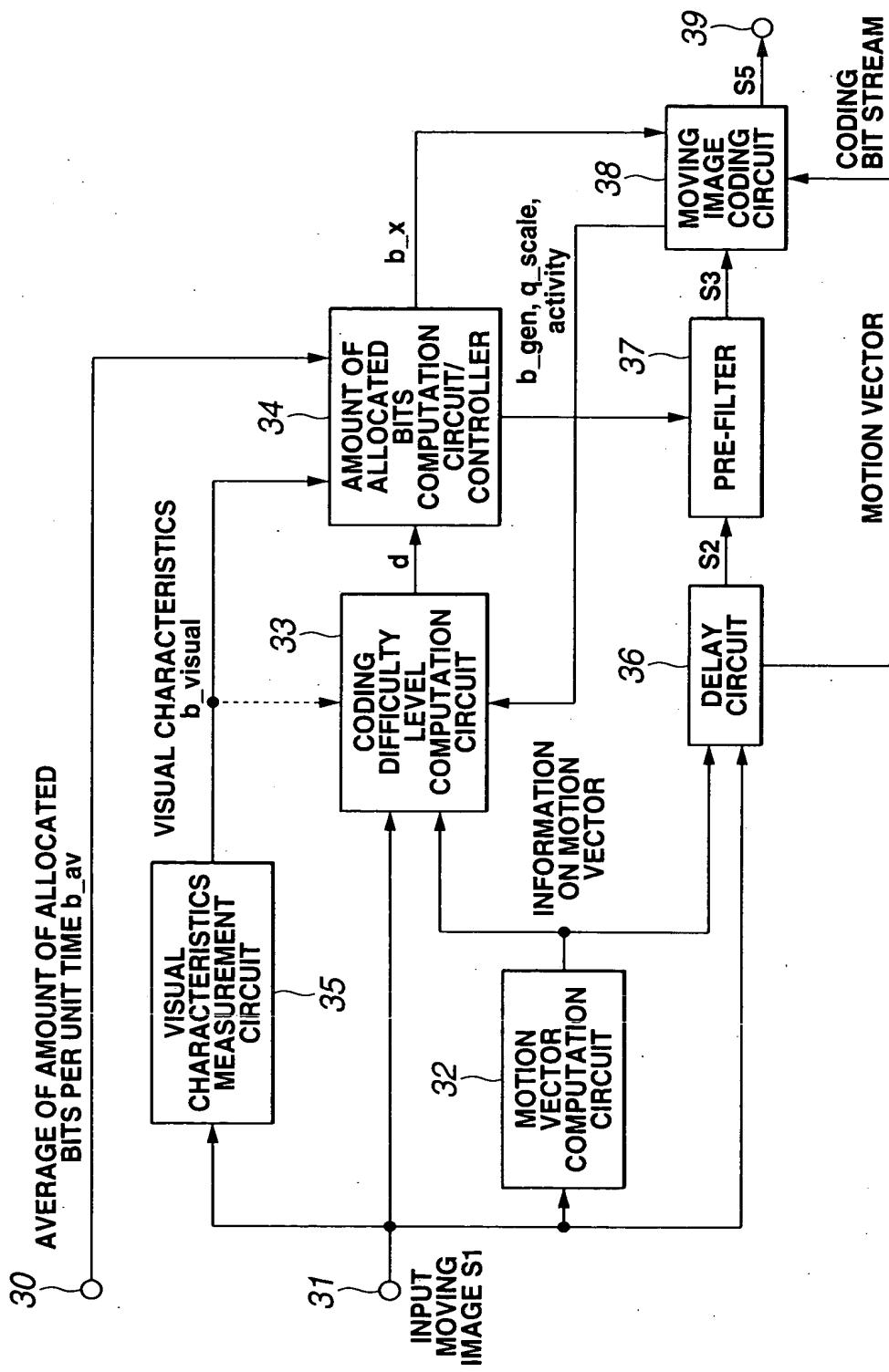


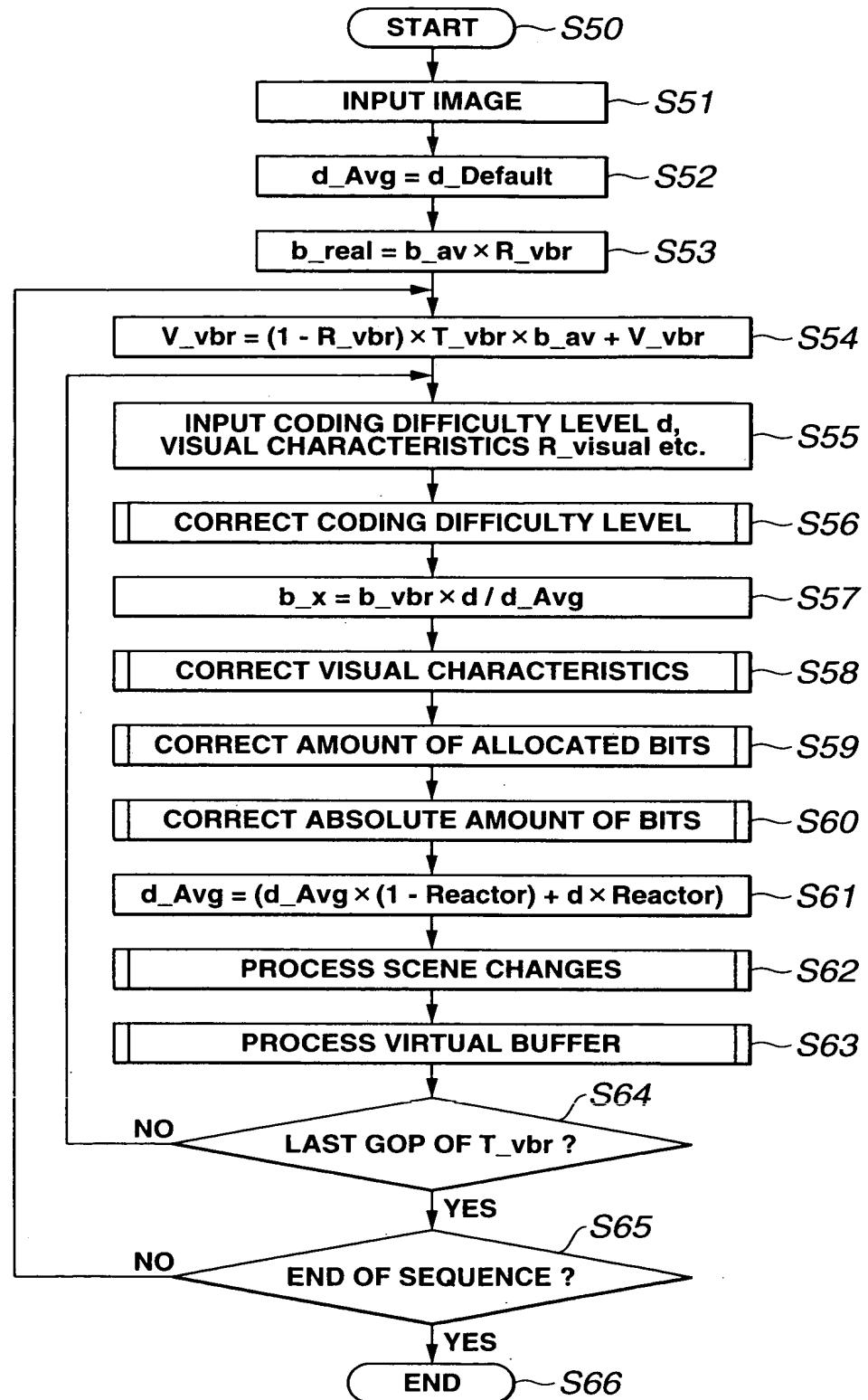
FIG.3

100 INPUT MOVING IMAGE S1
101 AVERAGE AMOUNT OF ALLOCATED BITS PER UNIT TIME b_{av}
102 INFORMATION ON MOTION VECTOR
103 CODING DIFFICULTY LEVEL COMPUTATION CIRCUIT
104 AMOUNT OF ALLOCATED BITS COMPUTATION CIRCUIT
105 CONTROLLER
106 DELAY CIRCUIT
107 PRE-FILTER
108 MOVING IMAGE CODING CIRCUIT
109 CODING BIT STREAM
S1
S2
S3
S4
S5
 b_x
 b_{gen}

FIG.4

FIG.5



**FIG.6**

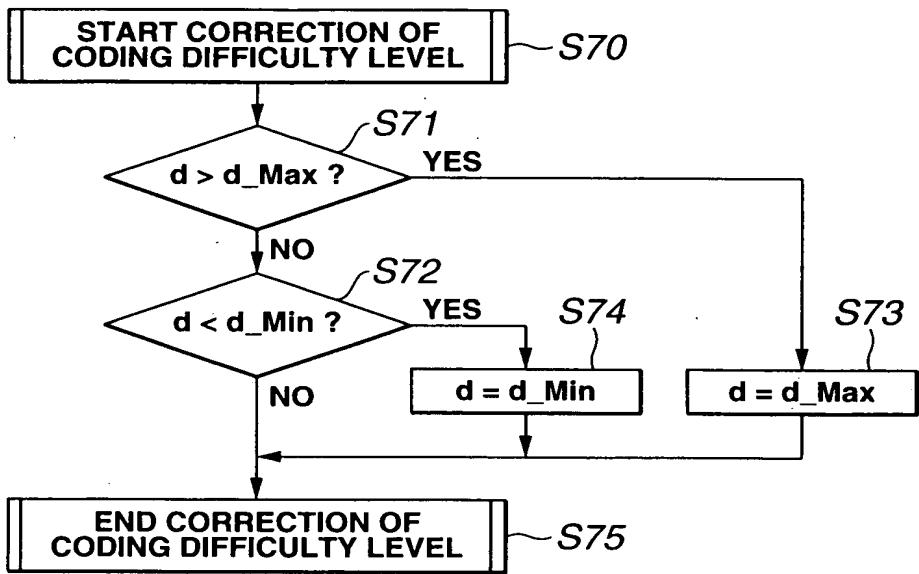


FIG.7

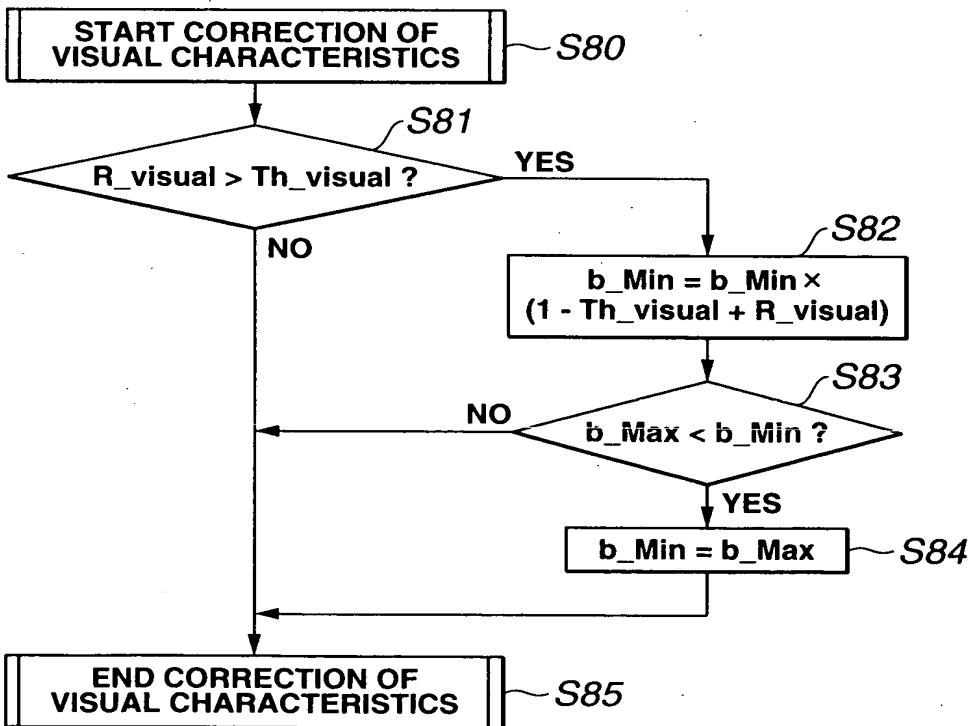


FIG.8

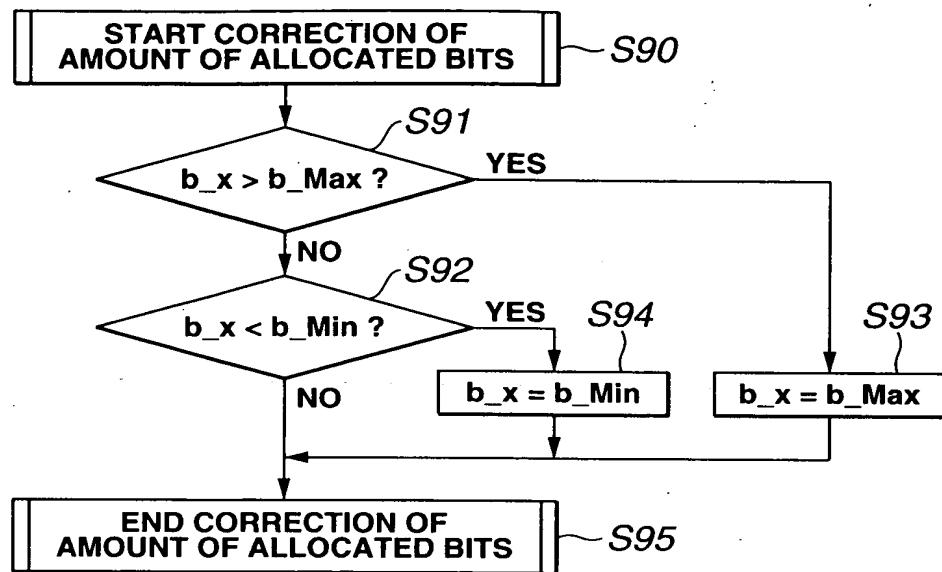
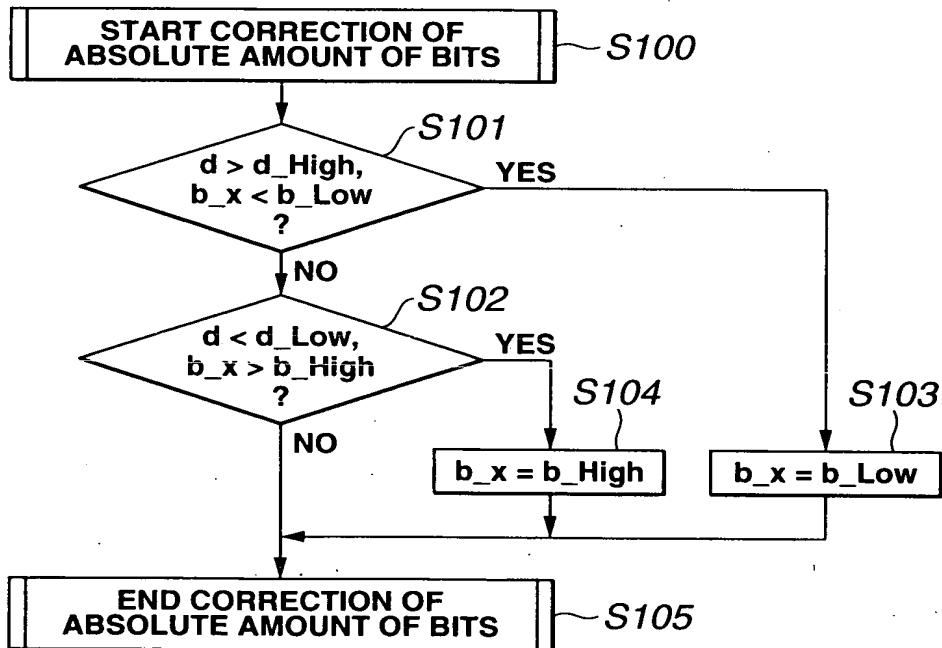
**FIG.9****FIG.10**

FIGURE 10 15000650

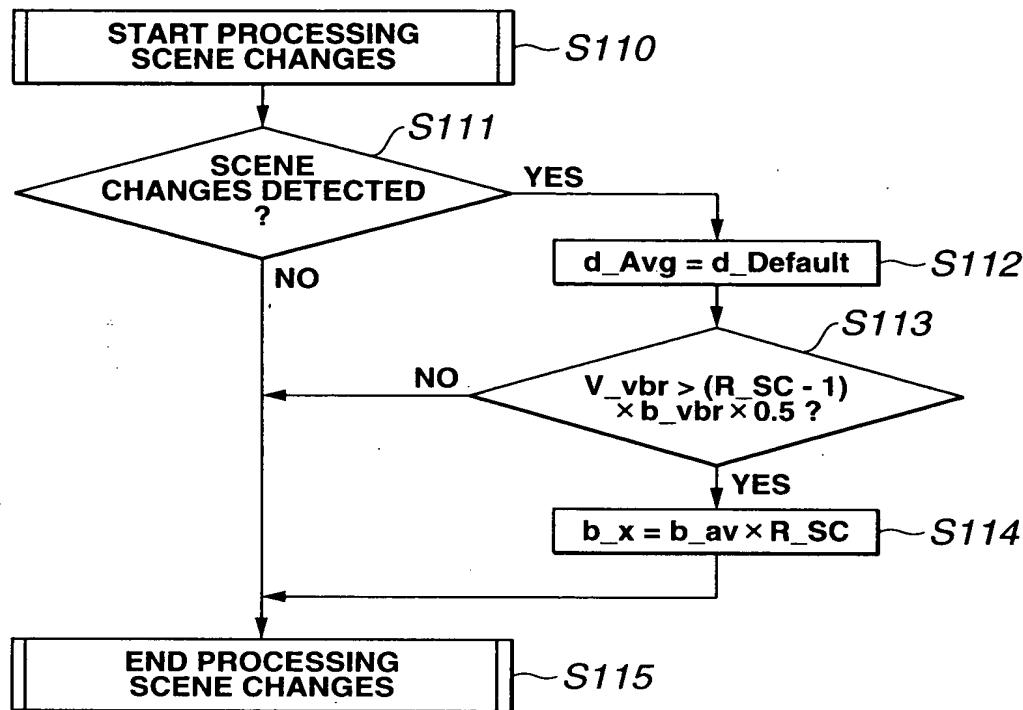


FIG.11

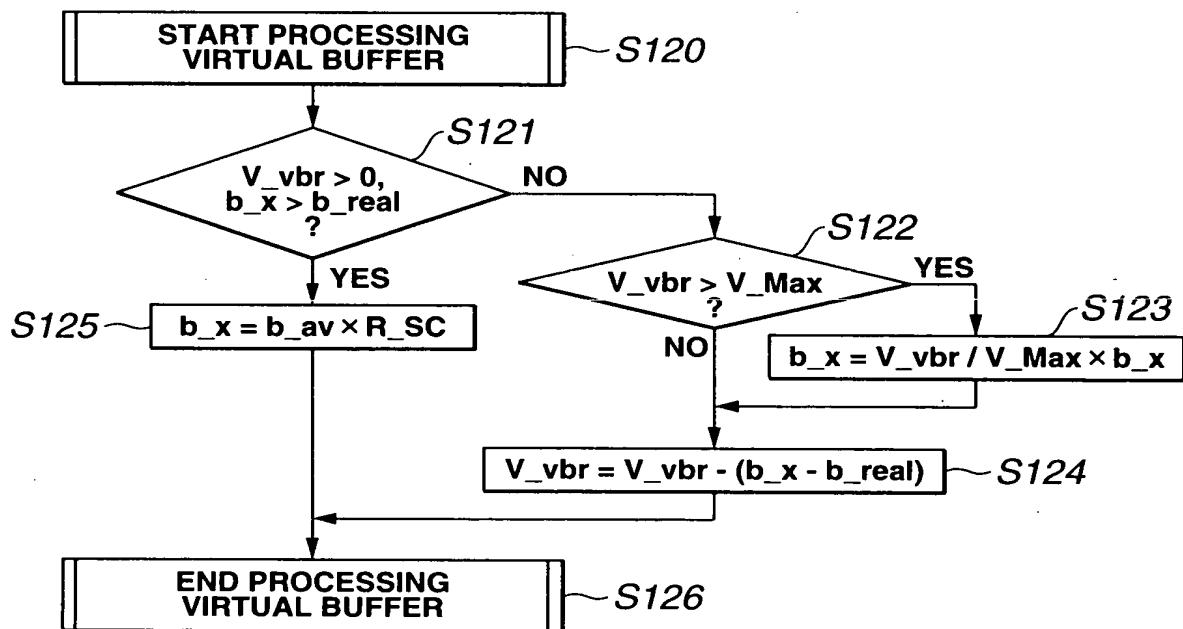


FIG.12

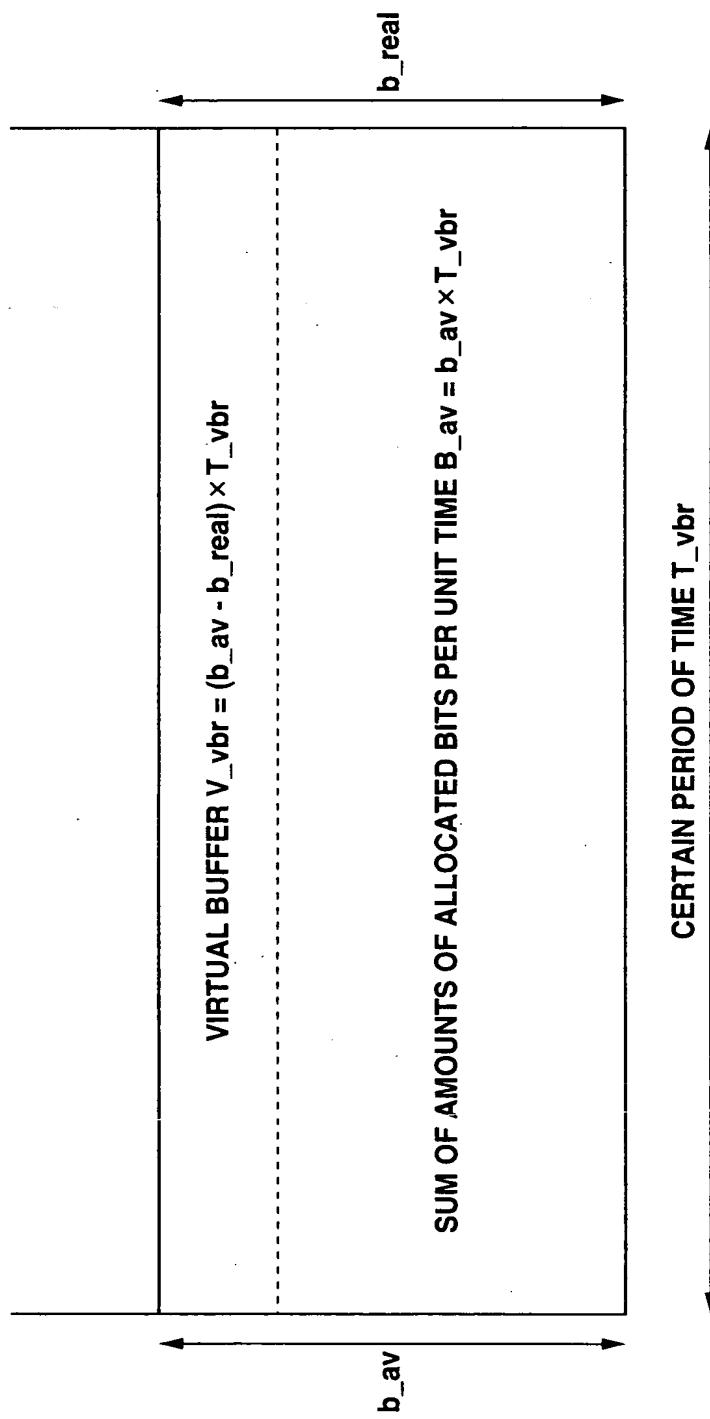
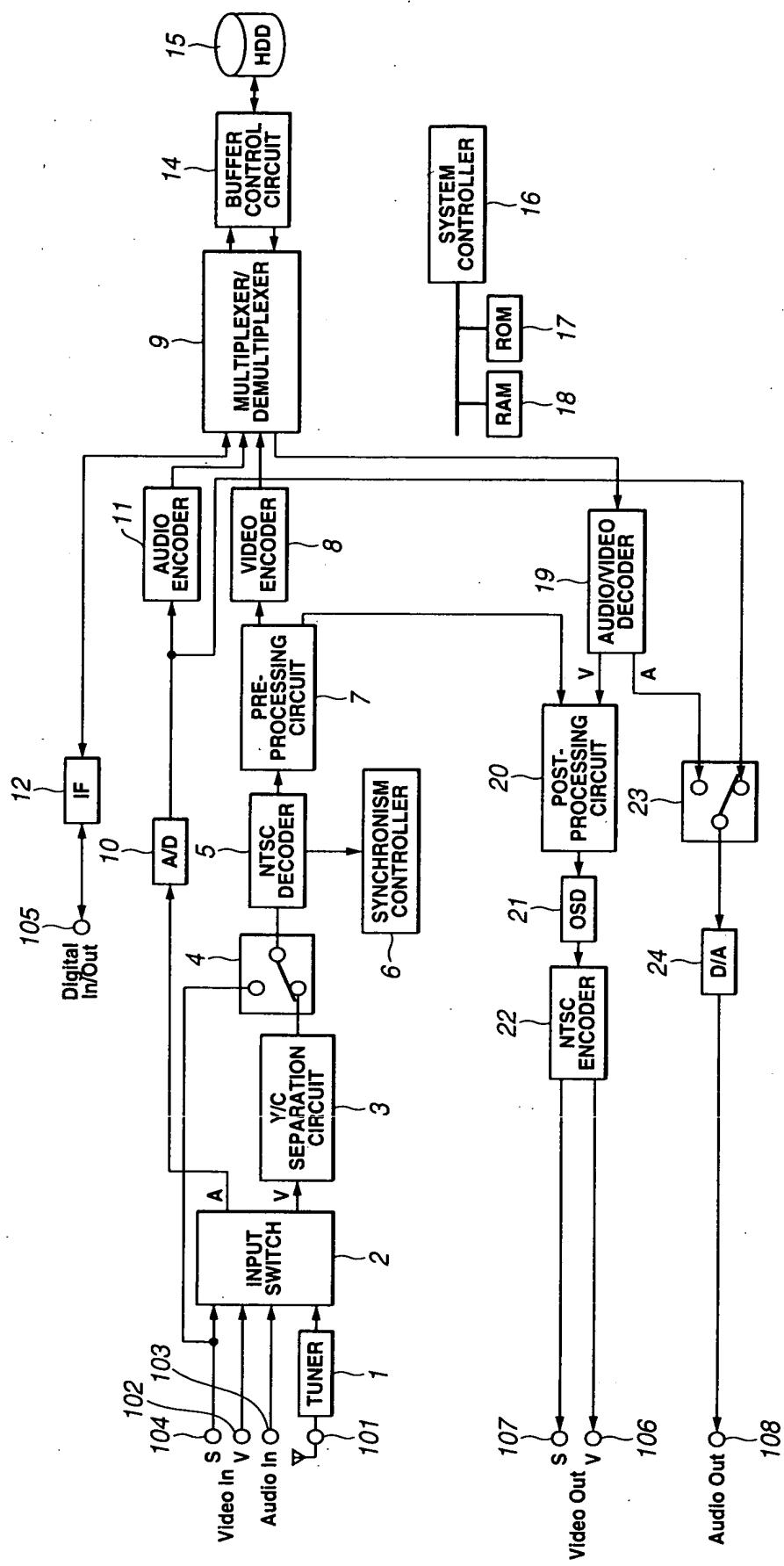


FIG.13

**FIG.14**